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		Application Number	09/629,337
		Filing Date	August 1, 2000
		First Named Inventor	Mark C. Fowler
		Art Unit	2672
		Examiner Name	Jeffery A. Brier
Sheet 1	of 1	Attorney Docket Number	0100.0001160

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
JB		PINEDA, JUAN; A Parallel Algorithm for Polygon Rasterization; Computer Graphics; Volume 22, Number 4; August 1988; pages 17-20.	
JB		AKELEY, KURT; RealityEngine Graphics; Silicon Graphics Computer Systems; Computer Graphics Proceedings; Annual Conference Series; 1993; pages 109-116.	
JB		VAN HOOK, TIMOTHY J. et al.; Method and Apparatus for Graphics Rasterization; patent application filed December 28, 1999.	

Examiner Signature	/Jeffery Brier/	Date Considered	01/23/2007
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